

J1040 U.S. PTO  
10/072261

02/07/02

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU  | EXAMINER |
|----------|-------------|-------|----------|------|----------|
| 10072261 | 02/07/2002  | 206   | 2        | 3727 |          |

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\*\*CONTINUING DATA VERIFIED:

No B. Palmer

\*\* FOREIGN APPLICATIONS VERIFIED:

No B. Palmer

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no

35 USC 119 conditions met ☐ yes ☒ no

Verified and Acknowledged Examiners's initials B. Palmer

ATTORNEY DOCKET NO

10010971-1

TITLE : High-performance laminate for integrated circuit interconnection

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

| NOTICE OF ALLOWANCE MAILED   |           | Assistant Examiner | CLAIMS ALLOWED       |                               |
|--|-----------|--------------------|----------------------|-------------------------------|
|  |           |                    | Total Claims         | Print Claim for O.G. <u>2</u> |
| ISSUE FEE  |           | Primary Examiner   | DRAWING              |                               |
| Amount Due   | Date Paid |                    | Sheets Drwg.         | Figs. Drwg.                   |
| <input type="checkbox"/> TERMINAL<br>DISCLAIMER  |           | PREPARED FOR ISSUE | Application Examiner |                               |
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